

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Brian D. Trotter et al.
Assignee: Cirrus Logic, Inc.
Title: MULTIBIT DELTA-SIGMA MODULATOR WITH VARIABLE-LEVEL
QUANTIZER
Serial No.: to be assigned Filing Date: On or about October 31,
2003
Examiner: Group Art Unit:
Docket No.: 1451-CA

Austin, Texas
October 31, 2003

Mail Stop Patent Application
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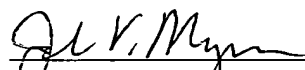
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Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above-identified patent application. Copies of any non-U.S. patents are enclosed.

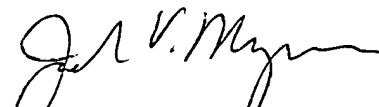
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I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450, on October 31, 2003.


Attorney for Applicant(s)

10/31/03
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Respectfully submitted,


Jack V. Musgrove
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Reg. No. 31,986

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Application Number	to be assigned
Filing Date	On or about 10/31/03
First Named Inventor	Brian Trotter et al.
Art Unit	
Examiner Name	
Attorney Docket Number	1451-CA

U. S. PATENT DOCUMENTS

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